ESDBEST ESD Bag Selection Guide

Purpose

This guide helps engineers and purchasing teams select the correct ESD protective bag for PCB assemblies, electronic components and finished products while complying with ANSI/ESD S20.20 and IEC 61340-5-1 ESD control programs.

Bag Type	Best For	Typical Surface Resistance	Notes
Metal-In Shielding	PCBs, ICs, export	Shielding construction	Highest protection against ESD event
Pink Anti-Static	Non-ESD sensitive packaging	10^9–10^11 Ω	No shielding, prevents tribocharging
Black Conductive	Component transport	10^3–10^5 Ω	For EPA use only
Moisture Barrier	MSL devices	Shielding + moisture barrier	Vacuum sealing supported

Selection Checklist

- Product sensitivity (HBM/CDM)
- Need shielding?
- Moisture sensitivity (MSL)?
- Bag size and thickness
- Open-top, zip-lock or vacuum style
- Printing, barcode or OEM branding

Recommended Applications

SMT production, PCB assembly, semiconductor packaging, aerospace electronics, medical electronics, automotive electronics and field service logistics.

Storage & Handling

Store bags in a clean, dry environment. Keep bags closed whenever possible. Verify compliance using an approved resistance meter and inspect bags regularly for damage.

Why Choose ESDBEST

- OEM/ODM manufacturing
- Custom sizes and printing
- Consistent quality inspection
- Bulk production for global B2B customers

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